

The diagram illustrates a multi-layered circuit structure, possibly a memory array or a multi-bit data path. It features several horizontal layers of rectangular blocks, each divided into smaller segments. The layers are labeled with various numbers and symbols:

- Top Layer (50):** A horizontal row of blocks, with a bracket labeled 52 above it. A label 54 is positioned above the middle of this layer. A label 56 is at the right end.
- Second Layer (30):** A horizontal row of blocks, with a bracket labeled 30 to its left. A label 36 is at the right end.
- Third Layer (32):** A horizontal row of blocks, with a bracket labeled 32 to its right. Each segment contains a curved arrow pointing left, labeled 70.
- Fourth Layer (20):** A horizontal row of blocks, with a bracket labeled 20 to its left. Each segment contains a curved arrow pointing left, labeled 70. A label 12 is at the left end, and a label 2 is at the right end.
- Fifth Layer (40):** A horizontal row of blocks, with a bracket labeled 40 to its left. A label 42 is at the right end.
- Sixth Layer (44):** A horizontal row of blocks, with a bracket labeled 44 to its right.
- Seventh Layer (46):** A horizontal row of blocks, with a bracket labeled 46 to its right.
- Bottom Layer (60):** A horizontal row of blocks, with a bracket labeled 60 to its left. A label 62 is at the left end, and a label 64 is at the right end. A label 66 is at the bottom right, with an arrow pointing to the right labeled 2'.

On the right side, there are two vertical lines labeled **DLY-FORWARD** and **DLY-REVERSE**. A label **VSTOR** is positioned above the **DLY-FORWARD** line, and a label **TCK** is positioned above the **DLY-REVERSE** line. A large arrow points from the left towards the structure.

FIG. 1

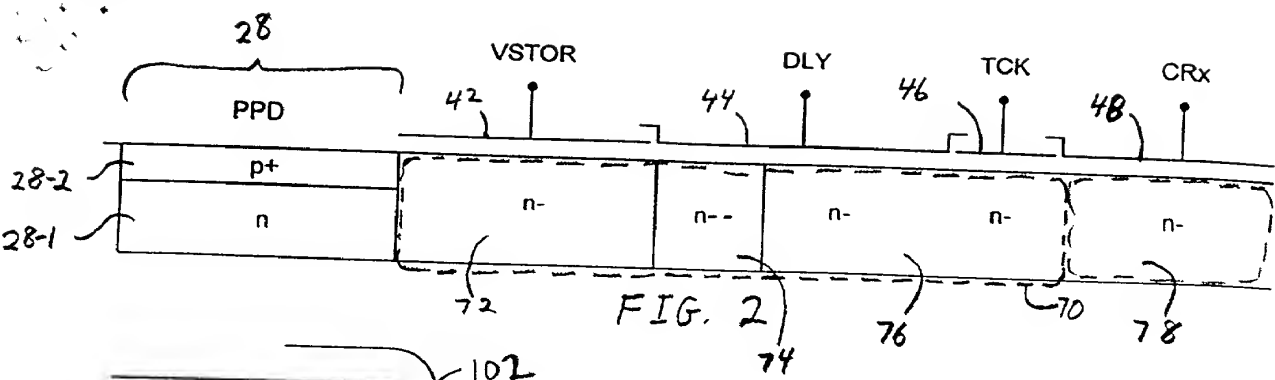


FIG. 2

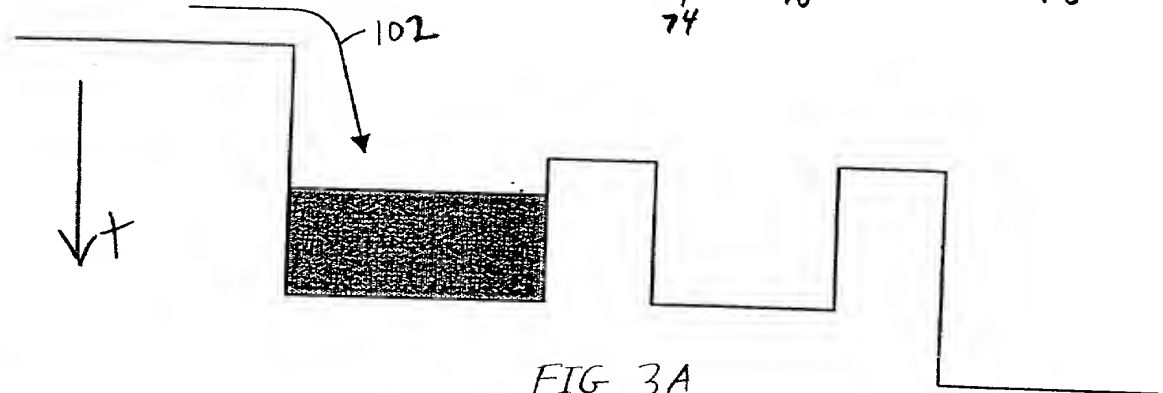


FIG. 3A

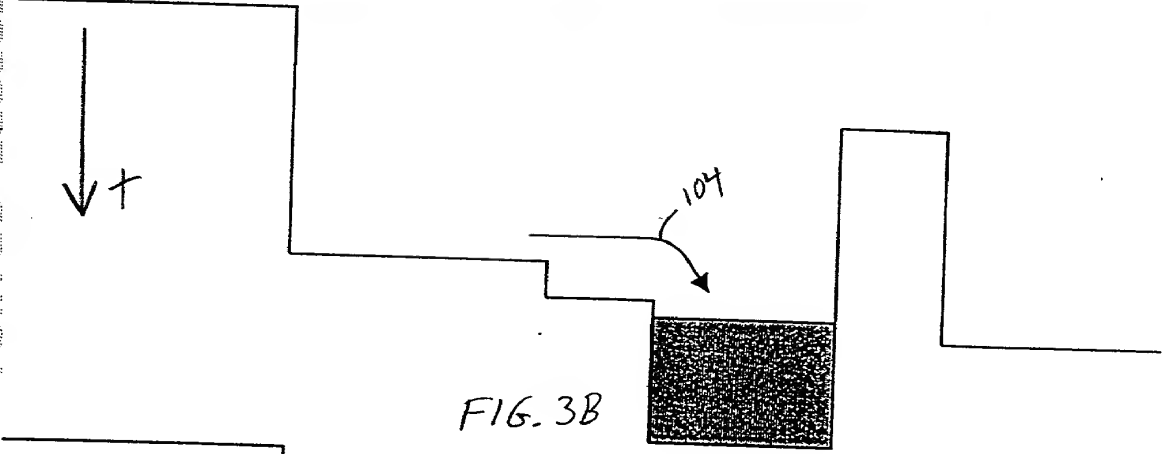


FIG. 3B

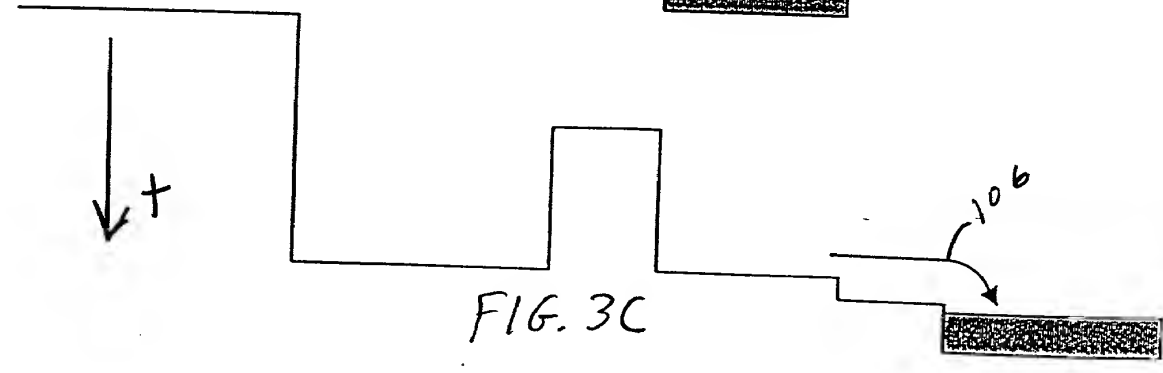


FIG. 3C

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DLY-FORWARD



TCK



DLY-REVERSE



FIG. 4

DLY-FORWARD



TCK



DLY-REVERSE



FIG. 5

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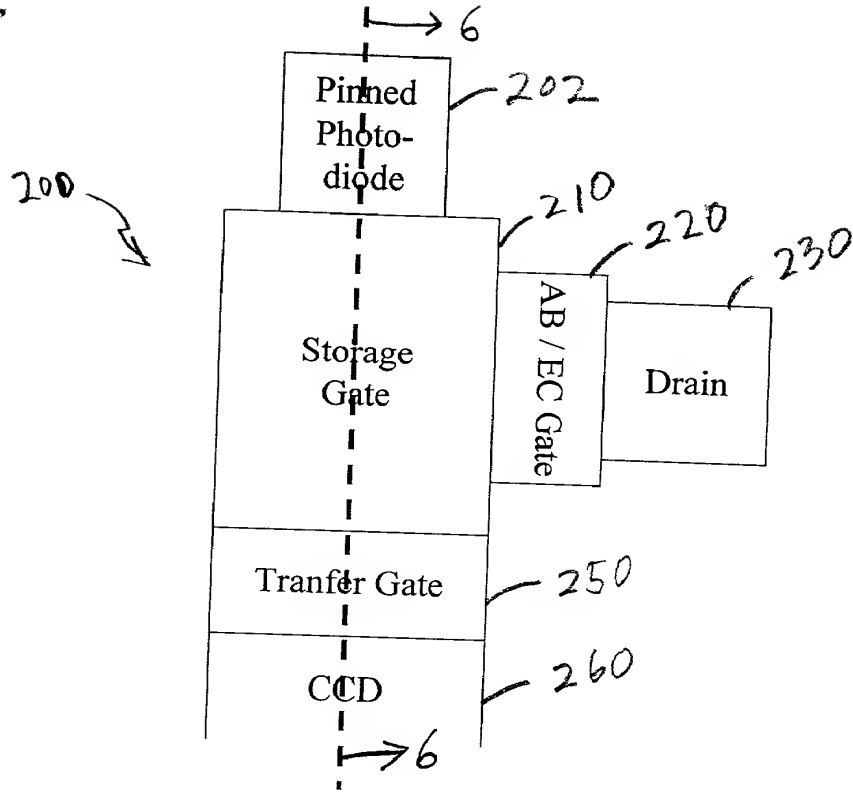


FIG. 6

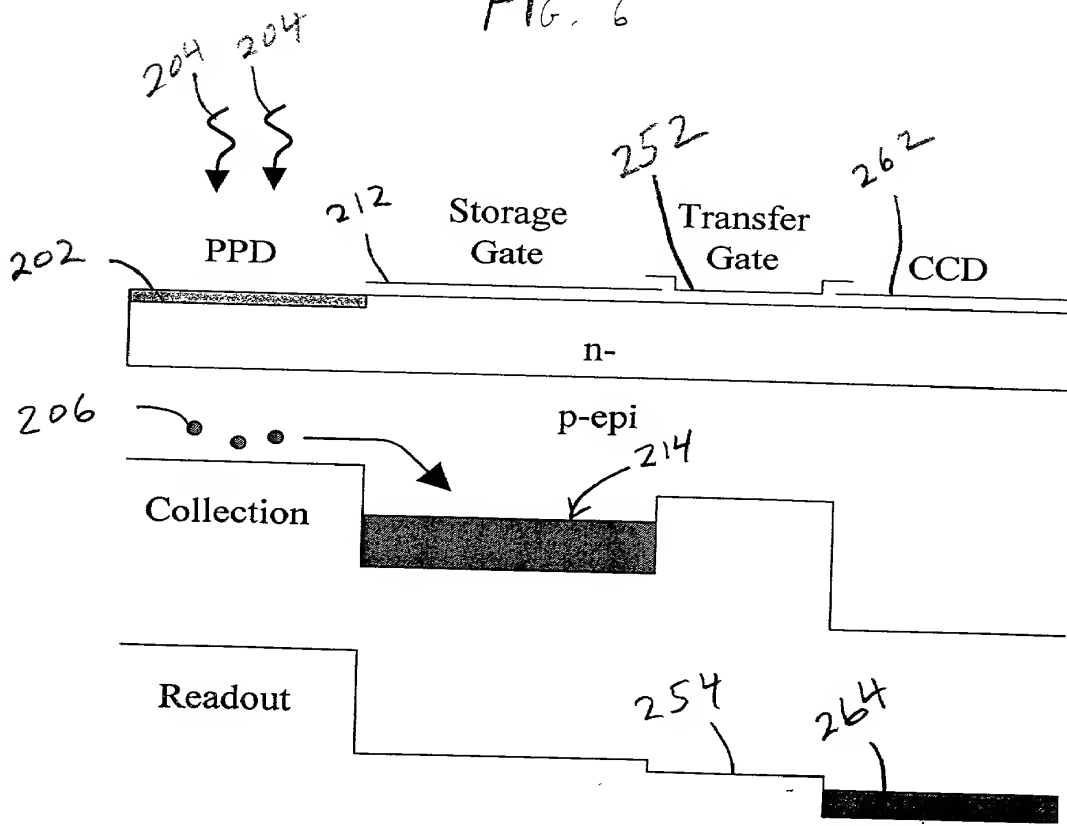


FIG. 7

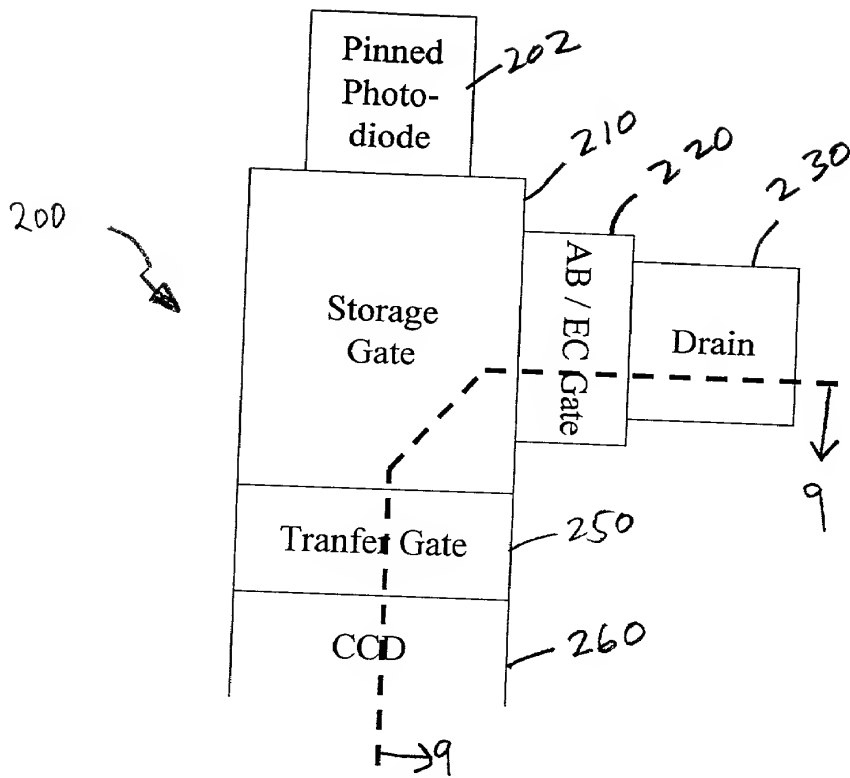


FIG. 8

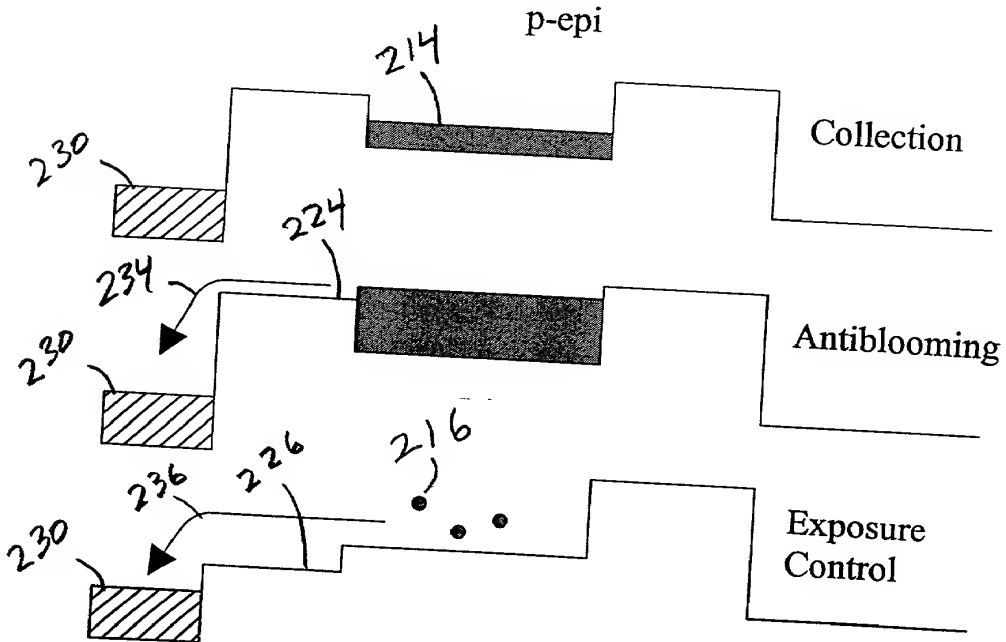
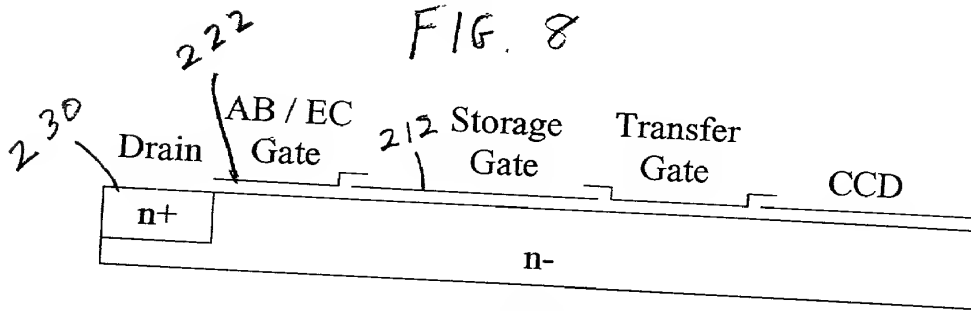


FIG. 9